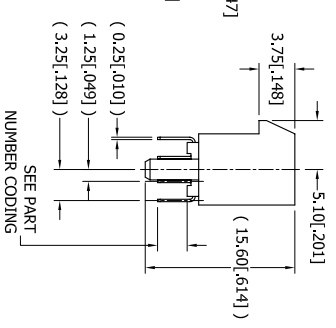
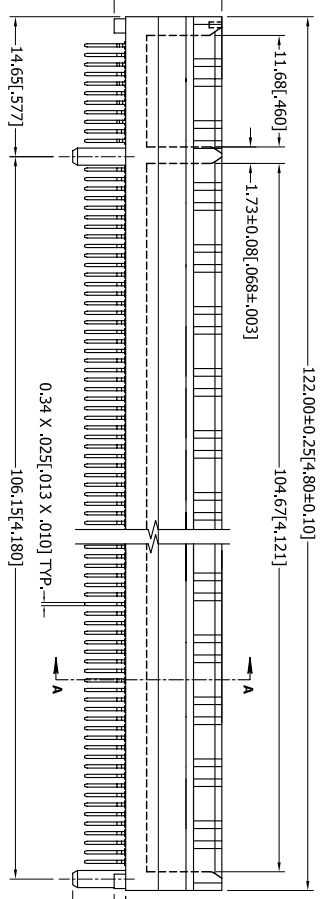
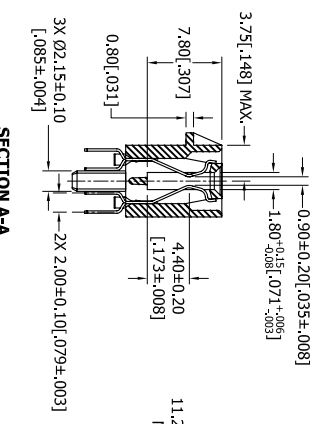
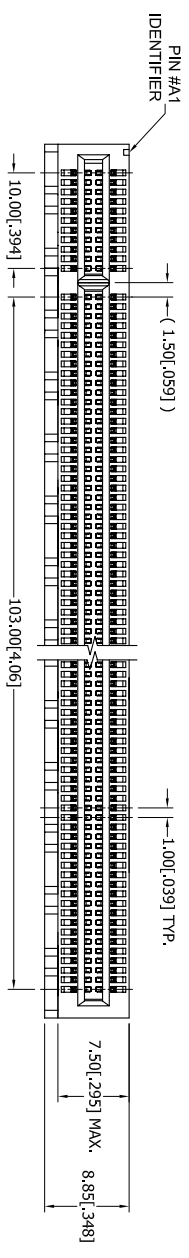
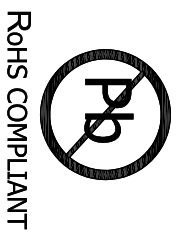


REVISONS		DATE	BY
REV.	ECO. NO	DESCRIPTION	
A	1595	INITIAL RELEASE	VJ
B	1707	UPDATE P/N CODING CHART, PLATING	ASK



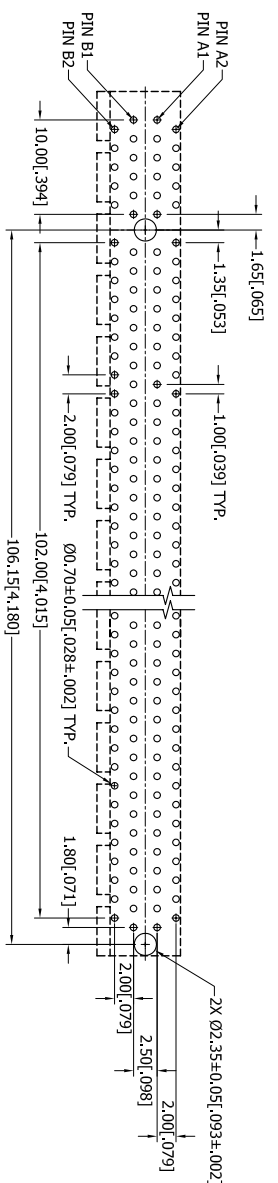
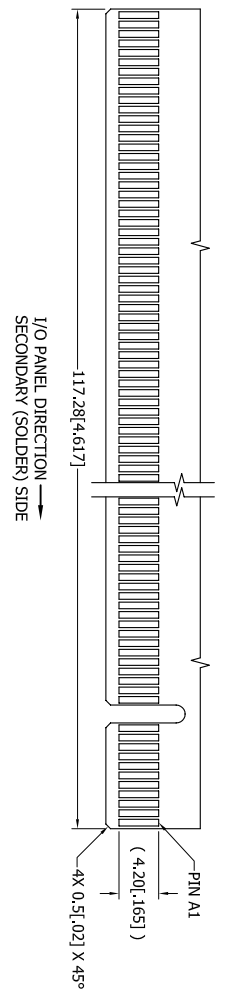
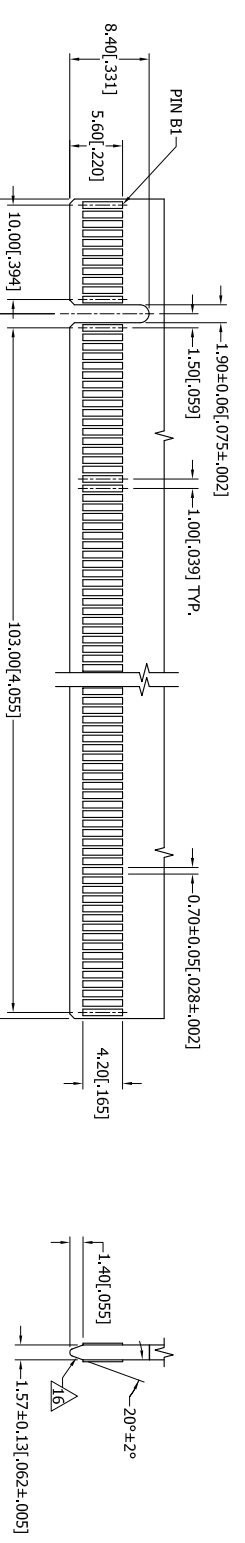
- NOTES:
- INSULATOR MATERIAL: HIGH TEMPERATURE THERMOPLASTIC.
 - CONTACT MATERIAL: SEE PART NUMBER CODING.
 - PLATING: SEE PART NUMBER CODING.
 - OPERATING TEMPERATURE: -10°C ~ 85°C.
 - PROCESSING TEMP: 260°C FOR 10 SECONDS MAX.
 - UL FLAMMABILITY RATING: 94V-0
 - VOLTAGE RATING: 50 VDC MINIMUM AT SEA LEVEL.
 - CURRENT RATING: 1.1 AMP PER CONTACT PAIR.
 - VOLTAGE DROP: 44 MILLI VOLT AT RATED CURRENT.
 - INSULATION RESISTANCE: 1000 MEGA OHMS.
 - CONNECTOR IDENTIFICATION: THE PART SHALL BE MARKED WITH A PART NUMBER AND BARCODE.
 - BOARD THICKNESS ACCOMMODATED: 1.57±0.20 [0.62±.008].
 - BOARD INSERTION FORCE: 1.15N (117.3)g MAX PER CONTACT PAIR WHEN USING A 1.57 [0.62] TEST BLADE.
 - BOARD WITHDRAWAL FORCE: 0.15N (15.3)g MINIMUM PER CONTACT PAIR USING 1.57 [0.62] PCB
 - MODIFICATION: SEE PART NUMBER CODING.

16. CHAMFER EDGES MUST BE FREE OF CUTTING BURR.



UNLESS OTHERWISE SPECIFIED:
 DIMENSIONS ARE IN INCHES(MM)
 ANGLARS: ° 30'
 XX±.02 (.508)
 XXX±.005 (.127)
 XXXX±.001 (.0254)
 SURFACE FINISH: G3 Ra
 REMOVE ALL BURS AND SHARP EDGES .010 MAX
 INTERPRET DIMENSIONS AND GEOMETRIC TOLERANCING PER: ANSI Y14.5M-1994

DRAWN	DATE	NAME	DESCRIPTION
04/01/08	V. JARA		THE INFORMATION HEREIN CONTAINS SULLINS ELECTRONICS AND IS NOT TO BE DISCLOSED TO OTHERS FOR ANY PURPOSES EXCEPT IN WRITING FROM AN OFFICE OF SULLINS ELECTRONICS.
PART NUMBER		VERTICAL, THRU HOLE, PC/E	
SIZE DWG. NO.		N_E1150HN-179	
SCALE: 1:1		11041	
SHEET 1 OF 2		REV	B



RECOMMEND PCB DIMENSIONS

PART NUMBER CODING

N E115DHHN - T9

PLATING — ALL PLATINGS ARE LEAD FREE AND HAVE .000050" NICKEL UNDERPLATE ON CONTACT SURFACE AND TERMINATION

CONTACT SURFACE
 B = .000010" GOLD
 C = .000030" GOLD
 W = GOLD FLASH

TERMINATION
 .000100" PURE TIN, MATTE
 .000100" PURE TIN, MATTE
 .000100" PURE TIN, MATTE

TAIL LENGTH
 1 = 2.30(.091)
 2 = 2.54(.100)
 3 = 3.00(.118) - 2.36(.093) PCB

COLOR
 1 = BLACK
 2 = BLUE
 3 = GREEN
 4 = PURPLE



RoHS COMPLIANT

UNLESS OTHERWISE SPECIFIED:
 DIMENSIONS ARE IN INCHES(MM)
 TOLERANCES:
 .XX = ±.005
 .XXX = ±.002
 .XXXX = ±.001
 .XXXXX = ±.0005
 SURFACE DIMENSIONS ARE MEASURED TO THE CENTER OF THE FEATURE UNLESS OTHERWISE SPECIFIED.
 REMOVE ALL BURRS AND SHARP EDGES .010 MAX.
 INTERPRET DIMENSIONS AND GEOMETRIC TOLERANCING PER: ANSI Y14.5M-1994

DATE	NAME	DESCRIPTION
04/07/08	VJAB	THE INFORMATION HEREIN CONSTITUTES A CONFIDENTIAL AND PROPRIETARY TRADE SECRET OF SULLINS ELECTRONICS AND IS NOT TO BE DISCLOSED, REPRODUCED, COPIED, OR TRANSMITTED IN ANY MANNER WITHOUT THE WRITTEN PERMISSION OF SULLINS ELECTRONICS.
		VERTICAL THRU HOLE, PCIE
		PART NUMBER
		N E115DHHN-T9
		SIZE DWG. NO.
		11041
		REV
		B

